

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT 	MMB DOCKET NO. 1890-0248	APPLICATION NO.: 10/537,029
	APPLICANT(S): Wu et al.	
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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
RJ	AA	5,799,051	Aug. 25, 1998	Leung et al.	—	—	
RJ	AB	5,600,280	Feb. 4, 1997	Zhang	—	—	
RJ	AC	5,206,609	Apr. 27, 1993	Mijuskovic	—	—	
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL						Yes No
	AM						Yes No
	AN						Yes No
	AO						Yes No
	AP						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

RJ	AQ		Maneatis, "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques", IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, Nov. 1996, pp. 1723-1732, (10 pages).
RJ	AR		Chan et al., "A 622-MHz Interpolating Ring VCO with Temperature Compensation and Jitter Analysis", IEEE International Symposium on Circuits and Systems, Jun. 1997, Hong Kong, pp. 25-28, (4 pages).
	AS		

EXAMINER /Ryan Johnson/	DATE CONSIDERED 12/19/2006
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicants.